

# H-A133L Mainboard Specification

Version	V1.0
Date	2023-07-18

敬告：本档版权归内容原创公司所有，并保留一切权力。档内容如有修改更新，请联系提供方获取最新本，恕不另行通知。

Note: This document is copyrighted by the content original company and all rights reserved. If the contents of the document are updated, please contact the provider for the latest version without notice.

# Changelog

1.0.0	2023-07-18	Chinese and English merged version.
-------	------------	-------------------------------------

# Contents

<b>1</b>	<b>PRODUCT OVERVIEW</b> .....	<b>5</b>
<b>2</b>	<b>SPECIFICATION LIST</b> .....	<b>7</b>
<b>3</b>	<b>INTERFACE DEFINITION</b> .....	<b>8</b>
➤	J1 DC-12V SOCKET .....	8
➤	J3 DC-12V INPUT HEADER.....	8
➤	J4 MIPI LCD PANEL FPC .....	8
➤	J5 RTC BATTERY HEADER .....	9
➤	J6 REMOTE CONTROL & LED HEADER.....	9
➤	J7 USB 2.0 OTG TYPE A .....	10
➤	J8 LED POWER SWITCH .....	10
➤	J9 LVDS VOLTAGE HEADER .....	10
➤	J10 LVDS HEADER.....	10
➤	J11 ETHERNET RJ45 JACK .....	11
➤	J12 POWER SWITCH & RESET HEADER.....	11
➤	J13 POE PD HEADER .....	11
➤	J14 SPEAKER HEADER.....	11
➤	J15 AUDIO LINE OUTPUT .....	12
➤	J16 KIO KEYPAD HEADER.....	12
➤	J17 DOOR CONTROL HEADER.....	12
➤	J18 DATA SERIAL PORT 2.....	13
➤	J19 BACKLIGHT CONTROL HEADER.....	13
➤	J20 MIC INPUT HEADER.....	13
➤	J21 I2C BUS HEADER .....	13
➤	J22 DATA SERIAL PORT 2.....	14
➤	J23 USB 2.0 HOST HEADER .....	14

---

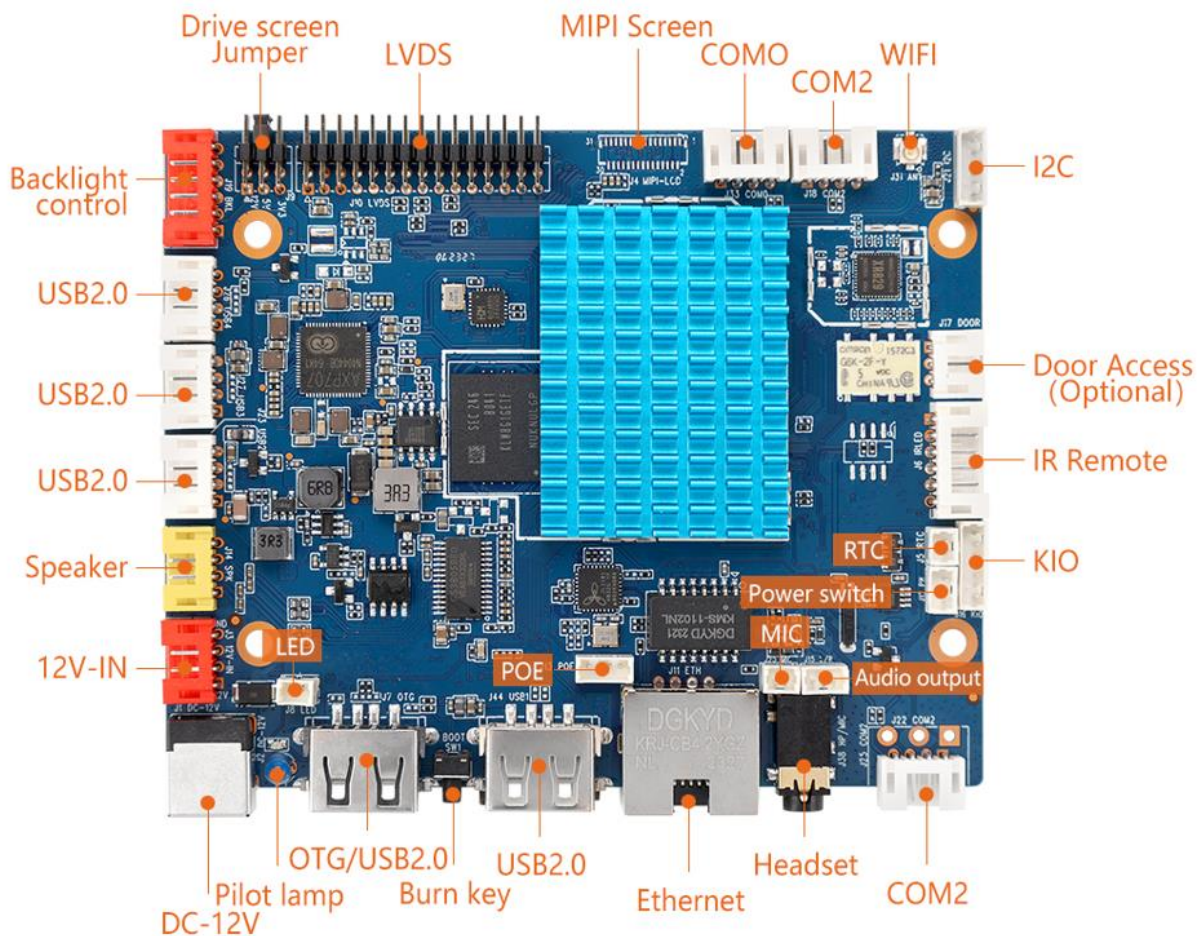
➤	J25	DATA SERIAL PORT 2 .....	14
➤	J26	USB 2.0 HOST HEADER.....	14
➤	J27	USB 2.0 HOST HEADER.....	15
➤	J28	TF CARD SOCKET.....	15
➤	J31	WiFi ANTENNA IPEX .....	15
➤	J33	DATA SERIAL PORT 0 .....	15
➤	J38	4-POLE HP/MIC JACK.....	16
➤	J44	USB 2.0 HOST TYPE A .....	16
➤	SW1	RECOVERY MODE BUTTON .....	16
<b>4</b>	<b>PHYSICAL SIZE</b> .....	<b>17</b>	
<b>5</b>	<b>ASSEMBLE PRECAUTIONS</b> .....	<b>18</b>	
<b>6</b>	<b>SOFTWARE GUIDE</b> .....	<b>19</b>	

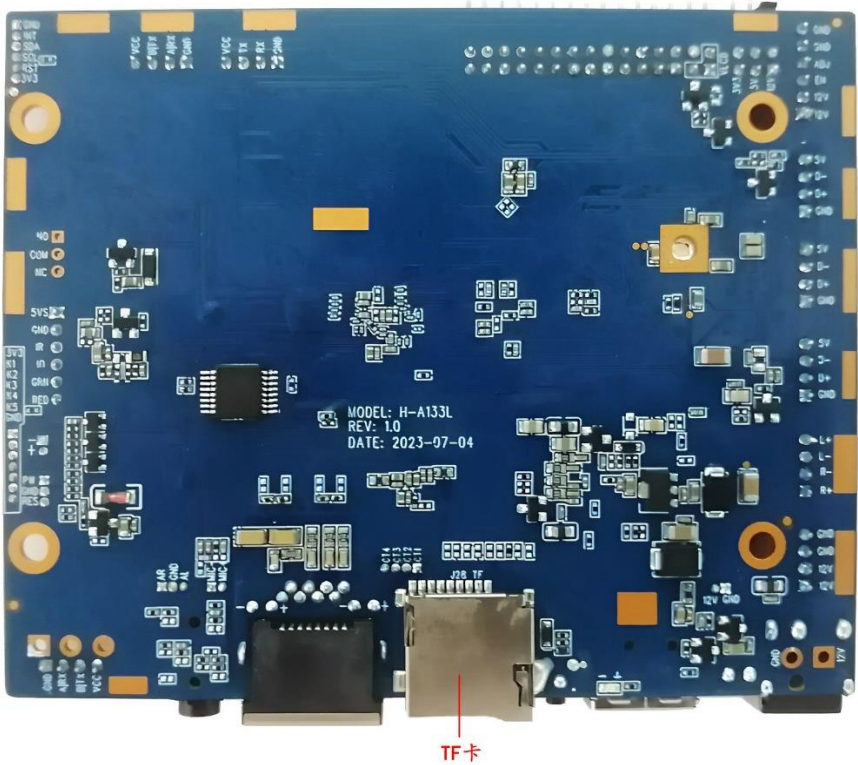
# 1 Product Overview

H-A133L mainboard is based on Allwinner A133 high-performance application processor platform. A133 is a low power, high performance processor for computing, personal mobile internet devices and other smart device applications. It integrates quad-core Cortex-A53 clocked at up to 1.6GHz, with superior computing performance, 2D/3D graphics processing capabilities and Full HD video codec capabilities. It perfectly supports 4Kx2K@30fps video decoding.

This mainboard is specially designed for **ultra-thin** applications with strict material selection and design. The compact size and rich interface facilitate its integration into the complete machine, bringing a smooth experience and superior performance to the final product. It can be applied to digital signage, touch interactive, consumer electronics, entertainment systems and other industries.

H-A133L V1.0 mainboard actual interface diagram as shown below.





## 2 Specification List

H-A133's system functions and interface features are shown in the following table.

Function & Interface	Detailed Description
<b>CPU</b>	Allwinner A133 Cortex-A53 quad-core, up to 1.6GHz
<b>DDR</b>	LPDDR4 1GB (2GB   4GB optional)
<b>Storage</b>	The default comes with an 8GB EMMC NAND chip that can scale up to 128GB
<b>LVDS</b>	30-pin industry-standard dual LVDS supporting VESA/JEITA format up to 1080P output
<b>MIPI-DSI Output</b>	31-Pin common MIPI DSI interface for extended MIPI panel sub-board
<b>HP/MIC</b>	Support CTIA 4-pole HP/MIC socket (Left-Right-GND-Mic)
<b>Line Output</b>	Support standard left and right channel line output (pin header)
<b>Amplifier output</b>	8 Ohm 6W Dual Audio Amplifier Output
<b>MIC Input</b>	Differential MIC input (pin header)
<b>USB Interface</b>	2 horizontal USB 2.0 connectors (Single Socket, one is for OTG), 3 pin headers
<b>Serial Port</b>	1 TTL/RS-232 compatible, 1 TTL/ RS-232/RS-485 compatible
<b>Micro SD Card</b>	Self-elastic micro SD card socket, up to 128GB capacity
<b>Camera</b>	Support USB camera within 2 million pixels
<b>WiFi</b>	Built-in high-performance SDIO interface WiFi module, supports IEEE 802.11 b/g/n/ac, default configuration single frequency 2.4GHz
<b>Bluetooth</b>	Built-in high performance serial interface BT module with support for V2.1+EDR/BT v3.0/BT v3.0+HS/BT v4.2
<b>Ethernet</b>	10/100M Adaptive Ethernet RJ45 connector + <b>4-Pin POE header</b>
<b>Backlight Control</b>	Industry standard LCD backlight control header, support for backlight switch and brightness adjustment
<b>Infrared RC</b>	Standard infrared remote control receiver and infrared receiver pin header
<b>GPIO Signals</b>	5-way GPIO signals for such as GPIO buttons and/or 3.3V digital input/output
<b>Door Control</b>	1 single-pole double-throw access control relay (optional)
<b>I2C Bus</b>	I2C pin header for I2C capacitive screen and etc
<b>Real Time Clock</b>	Ultra-low-power RTC circuit (CR1220 battery) with timer and alarm functionalities
<b>LED Indicator</b>	Blue LED indicator for running
<b>Buttons</b>	Recovery mode button and power switch button
<b>DC Input</b>	Supports 9~15V wide voltage DC power input
<b>Ambient Requirement</b>	Working temperature -20°C ~ 70°C, working humidity 0%~95% (non-condensing)
<b>Physical Size</b>	Length*Width*Height (100mm*80mm*9mm), <b>PCB top side height 7mm</b>
<b>Android Version</b>	Recommended Android 10, Linux optional ( <b>Not Ready</b> )

### 3 Interface definition

#### ➤ J1 DC-12V Socket

[J1] DC-12V power socket, positive outer and negative inner, inner pin diameter 2.0mm, outer ring diameter 5.5mm.

#### ➤ J2 DC-12V Input Header

[J2] DC-12V Input Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	12V	DC Power Input (9~15V)
2	GND	Power Ground

#### ➤ J3 DC-12V Input Header

[J3] DC-12V Input Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	12V	DC Power Input (9~15V)
2	12V	DC Power Input (9~15V)
3	GND	Power Ground
4	GND	Power Ground

#### ➤ J4 MIPI LCD Panel FPC

[J4] MIPI Panel LCD FPC Connector (FPC-0.3mm 31-Pin **Bottom Contact**).

Pin#	Definition	Note
1	LED+	LED Anode
2	LED+	LED Anode
3	LED+	LED Anode
4	NC	Not Connected
5	LED-	LED Cathode
6	LED-	LED Cathode
7	LED-	LED Cathode
8	LED-	LED Cathode
9	GND	Digital Ground
10	GND	Digital Ground
11	MIPI_D2P	+MIPI differential lane2



12	MIPI_D2N	-MIPI differential lane2
13	GND	Digital Ground
14	MIPI_D1P	+MIPI differential lane1
15	MIPI_D1N	-MIPI differential lane1
16	GND	Digital Ground
17	MIPI_CKP	+MIPI differential clock output
18	MIPI_CKN	-MIPI differential clock output
19	GND	Digital Ground
20	MIPI_D0P	+MIPI differential lane0
21	MIPI_D0N	-MIPI differential lane0
22	GND	Digital Ground
23	MIPI_D3P	+MIPI differential lane3
24	MIPI_D3N	-MIPI differential lane3
25	GND	Digital Ground
26	VDD-1V8	Power supply output 1.8V Power Supply 1.8V (not connected by default, need to solder R9232 0R)
27	RESET	Reset Signal in 1.8V
28	GND	Digital Ground
29	VDD-1V8	Power Supply 1.8V
30	VDD-3V3	Power Supply 3.3V
31	VDD-3V3	Power Supply 3.3V

### ➤ J5 RTC Battery Header

[J5] RTC Battery Header (SIP-1.25mm Square pad is pin 1).

Pin#	Definition	Note
1	BAT-	3V Coin Battery Negative
2	BAT+	3V Coin Battery Positive

### ➤ J6 Remote Control & LED Header

[J6] Remote Control & LED Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	5VS	Power output supply 5V standby
2	GND	Digital Ground
3	IR	5V level Irda remote control input signal
4	IO	3.3V level GPIO input signal
5	GREEN	Running indicator for external green LED
6	RED	Standby indicator for external red LED

## ➤ J7 USB 2.0 OTG Type A

[J7] USB 2.0 OTG Horizontal Type A Jack (Standard jack).

**Note:** This interface is connected to the internal USB0 signal and defaults to the firmware burning port when it is powered on. It can be connected to a PC for software burning; after entering Android, it can be set to the USB ADB debugging port or the ordinary USB Host interface through the software.

## ➤ J8 LED Power Switch

[J8] LED Power Switch (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Power Ground
2	12V	12V Switch Power Output (IO #363)

## ➤ J9 LVDS Voltage Header

[J9] LVDS Voltage Header (DIP 2.0mm-Square pad is pin 1). If pin 1 and 2 are jumper shorted, the VLCD of J10 is 12V. If pin 3 and 4 are jumper shorted, the VLCD of J10 is 5V. If pin 5 and 6 are jumper shorted, the VLCD of J10 is 3.3V. Please adjust the jumper position according to the actual logic voltage of the LCD screen. Be careful not to jumper to the wrong position or it may damage the LCD screen and the motherboard circuit.

## ➤ J10 LVDS Header

[J10] Dual LVDS header [DIP 2.0mm-Square pad is pin 1].

Pin#	Definition	Pin#	Definition
1	VLCD	2	VLCD
3	VLCD	4	GND
5	GND	6	GND
7	RXO0-	8	RXO0+
9	RXO1-	10	RXO1+
11	RXO2-	12	RXO2+
13	GND	14	GND
15	RXOC-	16	RXOC+
17	RXO3-	18	RXO3+
19	RXE0-	20	RXE0+
21	RXE1-	22	RXE1+

23	RXE2-	24	RXE2+
25	GND	26	GND
27	RXEC-	28	RXEC+
29	RXE3-	30	RXE3+

### ➤ J11 Ethernet RJ45 Jack

[J11] USB1x7 Hub port extended 100M Ethernet RJ45 Jack (Standard jack).

### ➤ J12 Power Switch & Reset Header

[J12] Power switch & reset Header (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	PW	Power on/off and screen on/off signal
2	GND	Digital Ground
3	RES	Hardware reset signal

### ➤ J13 POE PD Header

[J13] POE PD Header (SIP 2.0mm-Square pad is pin 1)

Pin#	Definition	Note
1	CT4	Transformer Center4
2	CT3	Transformer Center3
3	CT2	Transformer Center2
4	CT1	Transformer Center1

**Note:** The power supply of the POE powered interface comes from the J11 Ethernet port. This interface is connected to the POE powered conversion board for 12V power supply conversion. The current size of the 12V power supply is affected by the power supply capability of the POE switch and the conversion capability of the adapter board. The typical current is 1 ~1.5A. This interface supports POE power supply equipment with 1/2 wires of the network cable being positive and 3/6 wires being negative. It can also be connected to POE power supply equipment with 4/5 wires of the network cable being positive and 7/8 wires being negative.

### ➤ J14 Speaker Header

[J14] Speaker Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
------	------------	------

1	R+	Speaker right channel +
2	R-	Speaker right channel -
3	L-	Speaker left channel -
4	L+	Speaker left channel +

### ➤ J15 Audio Line Output

[J15] Audio Line Output (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	AR	Stereo output right channel
2	GND	Audio Ground
3	AL	Stereo output left channel

### ➤ J16 KIO Keypad Header

[J16] KIO Keypad Header (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	3V0	Power output supply 3.3V
2	K1	K1 (Regular GPIO #66)
3	K2	K2 (Regular GPIO #67)
4	K3	K3 (Regular GPIO #68)
5	K4	K4 (Regular GPIO #71)
6	K5	K5 (Regular GPIO #76)
7	GND	Digital Ground

Note: All KIO signals can be adjusted to keypad via a separated software version, such as K1 Volume+/K2 Volume-/K3 Standby/K4 Exit/K5 Home.

### ➤ J17 Door Control Header

[J17] Door Control header (DIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	NO	Access control is always on, software GPIO number 232
2	COM	Access control public terminal
3	NC	Access control public and closed terminals, default COM-NC conduction, software GPIO number 232

## ➤ J18 Data Serial Port 2

。 [J18] Built-in Serial Port 2 (SIP 2.0mm-Square pad is pin 1). The output level is TTL 3.3V by default and it could be setup to RS-232/RS-485 if required (RS-232 if U35 mounted, RS-485 if U67 mounted). **The related software device node name is ttyS2.**

Pin#	Definition	Note
1	GND	Digital Ground
2	RX A+	Data receive (TTL or RS-232 or RS-485 level)
3	TX B-	Data transmit (TTL or RS-232 or RS-485 level)
4	VCC	Power output (Default 3.3V, 5V option)

**Note: J18 COM2, J22 COM2 and J25 COM2 interfaces are internally multiplexed with the same set of signals and cannot be used at the same time.**

## ➤ J19 Backlight Control Header

[J19] Backlight Control Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	12V	If the current exceeds 2A, external 12V is recommended
2	12V	If the current exceeds 2A, it is recommended to connect an external 12V power supply
3	EN	The default output is 5V
4	ADJ	3.3V square wave (1KHz Freq.)
5	GND	Power Ground
6	GND	Power Ground

## ➤ J20 Mic Input Header

[J20] Audio input header (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Audio Ground
2	MIC	Mono microphone input

## ➤ J21 I2C Bus Header

[J21] I2C Bus Header (SIP 1.25mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Digital Ground

2	INT	Interrupt input (3.3V level)
3	SDA	I2C Bus data
4	SCL	I2C Bus clock signal
5	RST	Mainboard reset output (3.3V level)
6	3V3	Power output supply 3.3V

## ➤ J22 Data Serial Port 2

。 [J22] Built-in Serial Port 2 (SIP 3.81mm-Square pad is pin 1). The output level is TTL 3.3V by default and it could be setup to RS-232/RS-485 if required (RS-232 if U35 mounted, RS-485 if U67 mounted). **The related software device node name is ttyS2.**

Pin#	Definition	Note
1	RX A+	Data receive (TTL or RS-232 or RS-485 level)
2	GND	Digital Ground
3	TX B-	Data transmit (TTL or RS-232 or RS-485 level)

**Note: J18 COM2, J22 COM2 and J25 COM2 interfaces are internally multiplexed with the same set of signals and cannot be used at the same time.**

## ➤ J23 USB 2.0 Host Header

[J23] USB 2.0 Host Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Digital Ground
2	D+	USB Differential Data+
3	D-	USB Differential Data-
4	5V	Power output 5V

**Note: This USB interface is connected to the USB1 1x4 Hub set**

## ➤ J25 Data Serial Port 2

[J25] Built-in Serial Port 2 (SIP 2.0mm-Square pad is pin 1). The output level is TTL 3.3V by default and it could be setup to RS-232/RS-485 if required (RS-232 if U35 mounted, RS-485 if U67 mounted). **The related software device node name is ttyS2.**

Pin#	Definition	Note
1	GND	Digital Ground
2	RX A+	Data receive (TTL or RS-232 or RS-485 level)
3	TX B-	Data transmit (TTL or RS-232 or RS-485 level)
4	VCC	Power output (Default 3.3V, 5V option)

**Note: J18 COM2, J22 COM2 and J25 COM2 interfaces are internally multiplexed with the same set of signals and cannot be used at the same time.**

### ➤ J26 USB 2.0 Host Header

[J26] USB 2.0 Host Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Digital Ground
2	D+	USB Differential Data+
3	D-	USB Differential Data-
4	5V	Power output 5V

**Note: J18 COM2, J22 COM2 and J25 COM2 interfaces are internally multiplexed with the same set of signals and cannot be used at the same time.**

### ➤ J27 USB 2.0 Host Header

[J27] USB 2.0 Host Header (SIP 2.0mm-Square pad is pin 1).

Pin#	Definition	Note
1	GND	Digital Ground
2	D+	USB Differential Data+
3	D-	USB Differential Data-
4	5V	Power output 5V

**Note: This USB interface is connected to the 1x4 Hub group of USB1.**

### ➤ J28 TF Card Socket

[J28] Standard TF Card Socket.

### ➤ J31 WiFi Antenna IPEX

[J31] Standard IPEX antenna connector (Φ2.0mm).

### ➤ J33 Data Serial Port 0

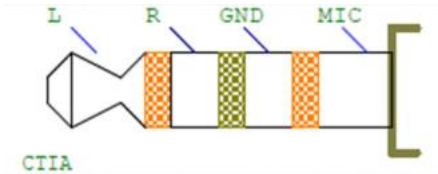
[J33] Built-in Serial Port 0 (SIP 2.0mm-Square pad is pin 1). The output level is TTL 3.3V by default and it could be setup to RS-232 if required (RS-232 if U35 mounted). **The related software device node name is ttyS0.**

Pin#	Definition	Note
1	GND	Digital Ground
2	RX	Data receive (TTL or RS-232 level)
3	TX	Data transmit (TTL or RS-232 level)
4	VCC	Power output (Default 3.3V, 5V option)

。 Note: If you need to use the built-in serial port 0 as a data serial port, please contact the supplier to obtain the customized software; this serial port will output the startup information in the first 5 seconds of power on (the upper or lower machine should handle this kind of data fault tolerance).

### ➤ J38 4-Pole HP/Mic Jack

[J38] 4-Pole 3.5mm Headphone & Microphone Jack (CTIA Standard jack). It is the same signals with J15/J20. It supports insert detection for speaker mute.



### ➤ J44 USB 2.0 Host Type A

[J44] USB 2.0 Host Horizontal Type A Jack (Standard jack).

**Note: This USB interface is connected to the 1x4 Hub group of USB1.**

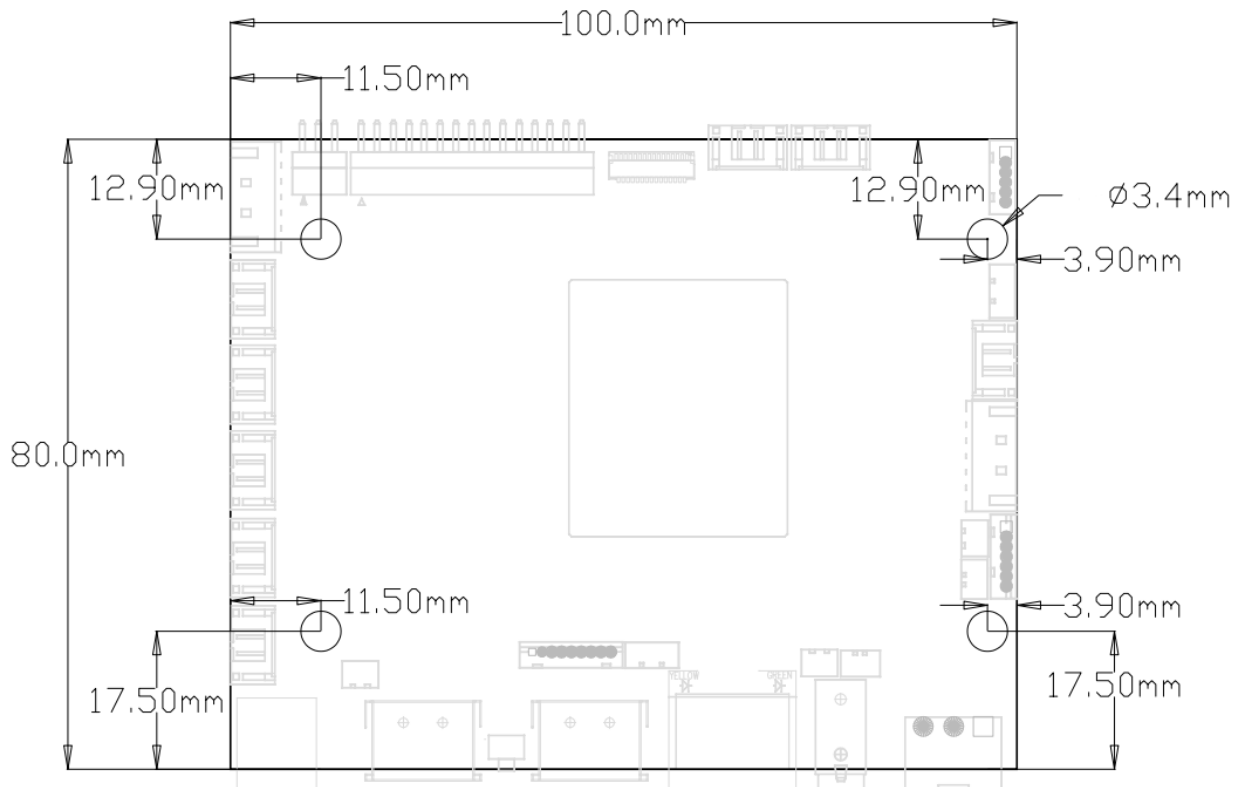
### ➤ SW1 Recovery Mode Button

[SW1] On-board recovery mode button. First press and then hold for about 3-second while power on will enter the recovery mode.



## 4 Physical Size

The PCB size is 100mm\*80mm, PCBA height is 7mm, fixed hole diameter is 3.4mm. The corresponding physical size parameters are shown in the figure below. For detailed size information, please consult the manufacturer for DXF file.



## 5 Assemble Precautions

Please note the following key points when using the H-A133L mainboard:

1. Relative humidity of this product: 10% to 90%, no condensation.
2. The working temperature of this product: -20°C ~ 70°C.
3. This storage temperature of this product: -40°C ~ 70°C.
4. Anti-static treatment is required during assembly and transportation of this product.
5. The board interface connection cable must not be too long. Otherwise, the signal quality may be affected.
6. Never allow the board to be distorted or heavily stressed during assembly.
7. Do not short circuit between mainboard and other peripherals.
8. When connecting to external LVDS or eDP LCD screen, pay attention to whether the screen voltage and current meet the requirements, and pay attention to the screen connector pin-1 direction.
9. When connecting to external LVDS or eDP LCD screen, pay attention to whether the backlight voltage and current meet the requirements. **If the LCD backlight power is above 20W, it is recommended to use a separate power board for backlight power supply.**
10. When connecting to peripherals using USB, GPIO, Serial, I2C, SPI, HDMI, etc., pay attention to whether the IO voltage level and current of the peripheral meet the requirements. When using the power pin on these connectors to supply power to the external circuit, the regular power pin must not exceed 100mA, and the USB power pin must not exceed 500mA. When the serial port is connected to peripherals, level matching is also required (3.3V TTL level, RS-232 level and RS-485 level)
11. Please connect the power to the power input socket or connector, and evaluate whether the current of the whole board meets the requirements according to the total peripherals. It is strictly forbidden to directly supply power from the backlight connector.
12. The communication module should be mounted at least 5mm away from the metal housing to avoid signal interference.

## 6 Software Guide

**The H-A133L motherboard only supports single-screen LVDS/MIPI screen output and does not support dual-screen different display combinations.**

The H-A133L mainboard internal serial port and extended serial port software port numbers are as follows:

Port	Software Device Node
J33	/dev/ttyS0
J18/J22/J25	/dev/ttyS2